

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

First-Named Inventor: LEEDY

Application No.: 10/672,961 Conf.: 9439

Date Filed: 09-26-2003

Atty Docket No.: ELM-2 CONT. 4

Art Unit: 2894

Examiner: LEWIS, MONICA

Title: THREE DIMENSIONAL MULTI LAYER MEMORY AND CONTROL LOGIC  
INTEGRATED CIRCUIT STRUCTURE

Commissioner for Patents  
P.O. Box 1450  
Arlington, VA 22313-1450

**NOTICE OF APPEAL**

Sir:

Applicant(s) hereby appeal(s) to the Board of Patent Appeals and Interferences from the decision dated 10/10/2008 of the Examiner finally rejecting claims 88, 95, 106-109, 111-114, 116-123 and 125-128.

- ☐ Please charge the fee of \$500.00 to Deposit Account No. 50-4355.
- ☐ No additional fee is required, because the fee was paid in a prior appeal.

Please charge any fees which may be required, except the issue fee, or credit any overpayment to Deposit Account No. 50-4355.

Date: \_\_\_\_\_

Respectfully submitted,

By /Michael J. Ure/  
Michael J. Ure, Reg. No. 33,089

**CERTIFICATE OF MAILING or TRANSMISSION**

I hereby certify that this correspondence is being:

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(Date) \_\_\_\_\_ (Signature) \_\_\_\_\_  
(Name) Michael J. Ure